

## CLAIMS

1. A signal processing system for processing signals outputted from a pointing device, said signal processing system comprising a first amplifier for amplifying a detection signal outputted by operating an operation console of the pointing device, along an x-axis direction thereof, a second amplifier for amplifying a detection signal outputted by operating the operation console, along a y-axis direction thereof, a first switching circuit for alternately switching over between respective output signals of the first and second amplifiers by the predetermined period before outputting, and a circuit for ac-grounding an output side of the first switching circuit for predetermined time upon switchover of the first switching circuit.

2. A signal processing system according to Claim 1, wherein the circuit for ac-grounding the output side of the first switching circuit comprises a first low pass filter made up of a resistor and a capacitor, connected between the output side of the first switching circuit and the ground, and a second switching circuit connected to both ends of the resistor, wherein the second switching circuit is turned on upon the switchover of the first switching circuit, and is turned off after the elapse of predetermined time from the switchover.

3. A signal processing system for processing signals outputted from a pointing device, the pointing device comprising detection means capable of outputting respective detection signals outputted by operating an operation console in plus and minus directions, along an x-axis, and a y-axis, thereof, respectively,

in such a way as to identify whether an operation is in either the plus direction or the minus direction, along the x-axis, and the y-axis, respectively, or in both the plus and minus directions, along the x-axis, and the y-axis, respectively, a first outputting means for fetching the detection signals outputted by the operation in either the plus direction or the minus direction, along the x-axis, and the y-axis, respectively, from the detection means, and a second outputting means for fetching the detection signals outputted by the operations in both the plus and minus directions, along the x-axis, and the y-axis, respectively, said signal processing system comprising a first switching circuit for alternately switching over between the detection signals by the operation along an x-axis direction, and a y-axis direction, respectively, outputted from the first outputting means, before outputting, a first amplifier for amplifying the detection signals by the operation along the x-axis direction, and the y-axis direction, respectively, outputted from the first switching circuit, a second amplifier for amplifying output signals of the second outputting means, a second switching circuit for alternately switching over between respective output signals of the first and second amplifiers by the predetermined period before outputting, and a circuit for ac-grounding an output side of the second switching circuit for predetermined time upon respective switchovers of the first and second switching circuits.

4. A signal processing system according to Claim 3, wherein the detection means comprises a first resistance element having a resistance value undergoing a change corresponding to a load applied thereto, by the operation in the plus direction, along the x-axis,

a second resistance element connected in series to the first resistance element, having a resistance value undergoing a change corresponding to a load applied thereto, by the operation in the minus direction, along the x-axis, a third resistance element having a resistance value undergoing a change corresponding to a load applied thereto, by the operation in the plus direction, along the y-axis, and a fourth resistance element connected in series to the third resistance element, having a resistance value undergoing a change corresponding to a load applied thereto, by the operation in the minus direction, along the y-axis, wherein a power supply is fed to one end of each of series-connected circuits, and a terminal connected to a node between the first and second resistance elements, and a terminal connected to a node between the third and fourth resistance elements serve as the first outputting means while a terminal connected to the end of each of the series-connected circuits, on a side adjacent to the power supply, serves as the second outputting means.

5. A signal processing system according to Claim 3, wherein the circuit for ac-grounding the output side of the first switching circuit comprises a first low pass filter made up of a resistor and a capacitor, connected between the output side of the second switching circuit and the ground, and a third switching circuit connected to both ends of the resistor, wherein the third switching circuit is turned on upon respective switchovers of the first and second switching circuits, and is turned off after the elapse of predetermined time from the respective switchovers.

6. A signal processing system according to Claim 2 or 5,

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further comprising a second low pass filter for removing low frequency noises of the output signal of the first amplifier, and a third low pass filter for removing low frequency noises of the output signal of the second amplifier, wherein an upper cut-off frequency of the first low pass filter is set lower than respective upper cut-off frequencies of the second, and third low pass filters.